# A Novel Technique for Mean Filtering Algorithms Based on Systolic Architecture

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Abstract: Mean filtering algorithms are widely used for image processing especially with image smoothing or denoising systems but its hardware implementation has many restrictions. Filtering Process needs image segmentation that was done by pipelined windowing technique to scan the processed image horizontally and vertically and the size of this window determines the length of mean filter. All pixels within the specified window are drawn simultaneously and processed together in each cycle. This process produces repeated calculations that leads to depleting more hardware which can be reduced highly if an efficient implementation method is used. Another important problem with traditional implementation method of this filter is the need of more I/O pins, since all pixel values of the processed window must be read at the same cycle. These I/O pins act as a big limitations that restrict the huge data manipulation systems when implemented with any high speed hardware platform. In this paper, two novel techniques were proposed to design and implement mean filtering algorithms, that ensure simplicity of the required hardware and efficiently improve processing time as well as the needed input pins are minimized highly by reading only the new pixels of the processed window, while keeping the remaining big part as it is without any change, since it is already prepared in previous cycle. The proposed techniques depend mainly on removing the repeated computations and cancellation of division operations which result in improving the processing efficiency and highly reduce the execution time to match real time requirements. Pipelining technique is also adopted here, with the second proposed technique, to activate parallel processing scheme. So, systolic architecture were used for further reduction in computation operations and to improve overall system execution time. The obtained results proved good enhancements in processing time especially for huge image size. The efficiency of implemented design was significantly improved for large filter length, that makes the proposed techniques very useful when dealing with huge data systems. The effect of Gaussian noise was tested for gray and colored images to compare the degradation of both of them with this type of noise.

Key words: Mean filtering, noise reduction, spatial filters, systolic array and parallel processing

# **INTRODUCTION**

Most image processing depend mainly on spatial filters that done by convolution masks. Image denoising, sharpening, blurring, edge detection and many other image processing necessitate using of masks. The mean filter is one of the most important masks that extensively used in different image processing (Jain, 1989).

Image filtering is the more important part in any image denoising system. The filtering process can be accomplished in frequency domain or in many cases, time domain filters can be very useful and mostly used for variety of image applications. Different spatial filters are used with image denoising system depending on the corrupted noise (Gonzalez and Woods, 2007).

If a Gaussian noise corrupt the original image, then mean filter is the most suitable one for noise removing in such cases. The efficiency of mean filter for noise removal with Gaussian noise refers to the fact that Gaussian noise values are concentrated around the mean as illustrated in Fig. 1 which shows the probability density function of Gaussian noise (Boncelet, 2000).



Fig. 1: Probability density function of Gaussian noise

When large images is considered to be processed and large window sizes is dependent then any application need these filters can be quite time consuming. For example, the basic implementation of a mean filter for a  $256 \times 256$  image with a 3\*3 window size, needs more than  $5 \times 10^5$  additions and also more than  $65*10^3$  divisions. We must exploit and assign the inherent redundancies in this traditional implementation method.



Fig. 2: Windowing technique

**Mean filter:** Mean filter is a linear digital filter that use a mask window of odd size to scan the image horizontally and vertically. The windowing operation is illustrated in Fig. 2.

Smoothing is done by reducing the variation in values for successive pixels. The smoothing operation is in fact equivalent to low-pass filtering that carefully smoothes any sudden change of pixel value by altering its value by average (mean) of its surrounding pixels. Smoothing filters or in more precisely mean filtering has a big area in noise removal and blurring of image processing (Kumar and Kumar, 2015).

Mean filtering is inherently a convolution operation as the mask is scanned the image until every pixel has been processed. Kernel is the base of convolutions process which is the shape and size of the surrounding neighbors that used for mean calculation. Large kernels are needed when smoothing must be deeply (Zhang and Li, 2014).

The traditional method, of mean filtering implementation is very simple but inefficient due to redundant and highly re-computations of the accomplished additions in the previous mask. The required number of additions is  $U*V*n^2$  and the needed division operations is U\*V only. Thus, the number of additions and divisions are related directly to the image size, so, the problem of high consuming time appear clearly with large images which is the important factor for real time processing.

A major redundancy caused by re-computation of the sum of all pixel values of the specified window when the mask is moved from specified pixel to the successive one. As the mask is shifted by one pixel the sum of the new column in the mask window is needed only, since, the sum of all other columns is already computed for the previous pixel.

**Mean filter implementation:** As shown in Fig. 2, the window of size n\*n firstly, scan the image horizontally

(a)			(b)		
Bef	ore filte	ring	Afi	ter filter	ing
3	2	8	3	2	8
4	9	6	4	5	6
7	5	1	7	5	1

Fig. 3: Mean	filtering process (a) Selected window and (	(b)
Mean	filtering process	

and when the processed raw of the specified image is completed, then it begins a vertical path to process the second raw of the noisy image and so on.

The mean filter process is in factthe computing of average value (mean) of all pixels of the specified window. So, a summation of all these pixels must be obtained. The obtained sum is used to compute the average value of the current mask window. Then the value of the centered pixel in the processed window must be replaced with this average.

The window usually has a square shape but it can be any other shape that has a symmetry around its center such as circular window and rhombical window shape or any other symmetrical geometric form. The shape of the used window affect the number of pixels that must be processed for each window, thus this number may be reduced which results in reducing the number of addition operations. Figure 3 shows an example of  $3\times3$  mean filter (taking specific window). The summation of all values in the given window is:

The mean (average ) value is:

45/9 = 5

The original value of the centered pixel is 9 and after mean filtering it was replaced by the mean of all nine values (Zhang and Li, 2014).

**Traditional mean filtering algorithm:** The classical method of mean filter implementation can be summarized in the following steps:

select a specific window size around the pixel; sum all the values in the mask; find the average of these values; replace the pixel value with mean value;

}

for every pixel in the image do;



Fig. 4: Traditional hardware implementation of mean filter

Hardware implementation of mean filter: The ordinary method for hardware implementation of mean filters is accomplished by scanning the window of specified size (n\*n) over the entire image horizontally and vertically. The number of pixels of each window is equal to n multiplied by n(n\*n pixels). All these pixels are entered simultaneously in parallel and summed together to compute the average of them. For n\*n window size it is needed (n\*n-1) adder and need n\*n input pins from the recommended hardware that used as an implementation platform. Figure 4 shows traditional hardware implementation of 3\*3 mean filter and it required eight adders. It is clear the number of required division operations in each step is only one.

### MATERIALS AND METHODS

**Proposed technique:** The proposed technique depends on the fact that the new window is highly similar to the previous one (Zhang and Li, 2014) (for 3\*3 mask window, similarity of 66.6%) and differs from it by only one column (also for 3\*3 mask window, difference ratio is 33.3). Then there is no need to enter all the pixels of the new window but only the column that represent the difference part between the previous and next windows should be entered. As well as the summation of the big parts of the new window need not to be calculated next time, since, it is exist from the summation process of the previous window. In this way, the number of required adders will be reduced from (n\*n-1) in the traditional technique to only (2\*(n-1)) with proposed techniques.

The proposed techniques deal with clock sequencing to decide the suitable position of the input column.

If mod(clock seq/3) = 1 then assign entered column 1 to first stage; If mod(clock seq/3) = 2 then assign entered column 2 to first stage; If mod(clock seq/3) = 0 then assign entered column 3 to first stage; } The hardware implementation of this improved method is well illustrated in Fig. 5.

**Proposed systolic technique:** Systolic architecture are used for further reduction in addition operations. This proposed approaches convert the concurrent processing to sequential form as a pipelined stages, each stage responsible on part of the complete job and deliver its output to the next stage synchronized by a system clock (Wan, 1996; Evans and Amin, 1995). The number of additions can be minimized using pipelining technique, to only (n-1) addition operation. The number of input pins is also minimized from (n\*n) pins to only (n) input pins with proposed techniques as shown in Fig. 6. The 5\*5 mean filter design based on systolic array has been shown in Fig. 7.

Avoidance of division operation: In this research, we want to simplify the structure of hardware implementation of mean filtering process via. the assistance of systolic array. The hardware simplification process start by minimizing the number of used adders to find the summation of all pixels values within the specified window. Now, another try has been made to avoid the division operation that is essential to compute the mean (average) from the obtained sum. The division process as its obvious, need a complicated hardware, so an alternate method is used to avoid this division process.

The pixel values of the gray image can have only integers values that varied from 0-255 for an 8-bit grayscale and refuse any value with floating point format but the division result can be floating point that mismatch the image coding and representation form so rounding the result must be done to convert it to an integer value. Figure 8 explains the process of determining the average, assume the average is X, from different expected sum's. Figure 8 shows that when having more than the half numbers of pixels for a specified value X (the used X value, here is 125) and the other values is for the previous or next value then all these sum's will be considered to have an average of X.

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Fig. 5: Proposed hardware implementation of mean filter



Fig. 6: Systolic design of mean filter with 3\*3 filter length



Fig. 7: Systolic design of mean filter with 5\*5 filter length

(a)											Sum	Mean
	125	125	125	125	125	124	124	124	124	┝	1121	124.5
urray	125	125	125	125	125	125	124	124	124	⊢	1122	124.6
1D-	125	125	125	125	125	125	125	124	124	┝	1123	124.7
ow by	125	125	125	125	125	125	125	125	124	┝╸	1124	124.8
wind	125	125	125	125	125	125	125	125	126	┣►	1125	125.0
lg any	125	125	125	125	125	125	125	125	126	┝→	1126	125.1
sentir	125	125	125	125	125	125	125	126	126	┣	1127	125.2
Repre	125	125	125	125	125	125	126	126	126	⊢►	1128	125.3
	125	125	125	125	125	126	126	126	126	┣	1129	125.4
(b)												
1112-1120			1	1121-1129		1130-1138					]	
					•		↓ ↓			,		_
		1	24		125			126		•••••		]

Fig. 8: Determination of mean value, (a) Actual mean for different sums and (b) Estimated mean for different ranges of sums without division operation

If the summation of the specified pixels vary between 1121-1129 then the average can be swing from 124.5-125.4 with step of 0.1 and all the values in this range can be considered as an integer of 125, since, any pixel value with floating point format can not be accepted for gray image. And when the sum vary between 1130-1138 the dependant average is 126 while when the sum is between 1112-1120, the average would be 124 and so on as illustrated in Fig. 8b.

**S/W and H/W implementation remarks:** The proposed techniques for mean filtering process using systolic array can be implemented easily by replacing the adders with their digital equivalent circuits. The pipelined behavior is solved by using systolic array design. The problem here concentrate with the implementing of circular switching circuit and the reordering and latch circuit. The job of these two circuits can be established by using suitable S/W that executed with any high speed processor. The required software code can be summarized as follows:

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\begin{array}{l} \mbox{PROCESS (clk)} \\ \mbox{BEGIN} \\ \mbox{IF rising_edge (clk) THEN} \\ \mbox{$i <= i+1$} \\ \mbox{$if i = 1$ THEN$} \\ \mbox{$C1 <= $SUM1$} \\ \mbox{$if i = 2$ THEN$} \\ \mbox{$C2 <= $SUM1$} \\ \mbox{$if i = 3$ THEN$} \\ \mbox{$C3 <= $SUM1$} \\ \mbox{$i <= 0$} \\ \mbox{END PROCESS} \end{array}
```

#### **RESULTS AND DISCUSSION**

The two proposed methods were tested for 8-bit grayscale images and then extended for colored images filtration. Tested images have different sizes starting by 64\*64 and ending with image size of 1024\*1024. Mean filtering was applied using three different methods:

- C Traditional method
- C Proposed method
- C Developed method using systolic array architecture

For all these methods the testing is considered for varying masking window size sewing from 3\*3-11\*11 window size. The formulated used number of adders for traditional and proposed methods are summarized in Table 1. This table presents also the number of required division operation for each design method.

To declare the comparison among the three tested methods, the used numbers of adders were computed to



Fig. 9: Number of addition operations with respect to varying image size for different techniques



Fig. 10: Number of addition operations with respect to different techniques for 1024\*1024 image size

single window for each method with respect to different sizes of masking window vary from 3\*3 and ending with 11\*11 window size as illustrated in Table 2.

Different images with variable size ranged from 64\*64 to 1024\*1024 were processed in this work, the above calculations were repeated for each one and presented in Table 3.

Processing time was computed for mean filter with different filter length for each implemented method in this paper (traditional and proposed methods). Table 4 presents the computed values of this processing time.

The varying number of adders for each image by applying all the implemented methods in the present research are shown in Fig. 9 while Fig. 10 repeat the same graph but for the image of size 1024\*1024 only.

The increasing number of addition operations according to window size for the three tested methods is will illustrated in plot of Fig. 11 and the inverse plot that shows the number of addition operations with respect to these implementing methods against different window size is shown in Fig. 12 that applied to largest image also.

The plot of execution time against varying window size for the largest test image which have a size of 1024\*1024 is shown in Fig. 13.

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Table 1: Number of adde	rs for traditional and propos	ed methods			
Technique/Operators	Traditional method	Improved method	Systolic method	Advanced method	Advanced systolic
Adders n*n-1		2*(n-1) n-1		Ν	1
Dividers	1	0	0	0	0
Table 2: The number of r Used method/Window siz	equired adders according to ze Traditional n	filter length for different internet for the second	implementing techniques posed method	Proposed met	hod with systolic array
3*3	8 4		4	2	
5*5 24			8	4	
7*7	48		12	6	
9*9	80		16	8	
11*11 120			20		10

Table 3: Approximated number of required addition operations for different image size with different design techniques

	Traditional method			Proposed method			Proposed method with systolic array		
Used method/Image size	3*3	5*5	7*7	3*3	5*5	7*7	3*3	5*5	7*7
64*64	33*10 <sup>3</sup>	99*10 <sup>3</sup>	20*104	16*10 <sup>3</sup>	32*10 <sup>3</sup>	49*10 <sup>3</sup>	8*10 <sup>3</sup>	16*10 <sup>3</sup>	24*10 <sup>3</sup>
128*128	$13*10^{4}$	$40*10^{4}$	79*10 <sup>4</sup>	65*10 <sup>3</sup>	$13*10^{4}$	$20*10^4$	32*10 <sup>3</sup>	65*10 <sup>3</sup>	98*10 <sup>3</sup>
256*256	53*10 <sup>4</sup>	16*10 <sup>5</sup>	3*106	$26*10^4$	53*10 <sup>4</sup>	$79*10^{4}$	$13*10^{4}$	$26*10^4$	39*10 <sup>4</sup>
512*512	$2*10^{6}$	$6*10^{6}$	12*106	$1*10^{6}$	$2*10^{6}$	3*106	5*10 <sup>5</sup>	$1*10^{6}$	15*10 <sup>5</sup>
1024*1024	9*10 <sup>6</sup>	25*10 <sup>6</sup>	50*10 <sup>6</sup>	4*10 <sup>6</sup>	$8*10^{6}$	12*10 <sup>6</sup>	$2*10^{6}$	$4*10^{6}$	6*10 <sup>6</sup>

Table 4: Processing time for varying filter length with respect to different techniques

Used method/Window size	Traditional method	Proposed method	Proposed method with systolic array
3*3	0.032768	0.0182044	0.010113
5*5	0.055945	0.019980	0.011100
7*7	0.080372	0.021150	0.011750
9*9	0.125998	0.026249	0.014583
11*11	0.183752	0.031681	0.017600



Fig. 11: Number of addition operations with respect to different techniques for varying filter length for an image of size 1024\*1024







Fig. 13: Processing time for varying filter length with respect to different techniques

The plots that shows the big arising in number of addition operations with respect to image size for different window size are shown in Fig. 14-16 for traditional, improved and systolic method, respectively. The curves of Fig. 15 and Fig. 16 have the same shape but with different values.

Figure 17 shows the tested grayscale images which are chosen for four children with different ages, so the face details of them will be significantly different. Image 1 is for the younger child and image 2 is for the second one and so on, so, image 4 is for the biggest child. Testing of mean filtering process with colored images was shown in Fig. 18.

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Fig. 14: Number of addition operations with respect to varying image size for traditional technique according to different filter length

Fig. 15: Number of addition operations with respect to varying image size for improved technique according to different filter length



Fig. 16: Number of addition operations with respect to varying image size for systolic technique according to different filter length



Fig. 17: Continue

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Fig. 17: Performance of implemented mean filter with gray images (a) Original images, (b) Noisy images and (c) Filtered images



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Fig. 18: Performance of implemented mean filter with colored images, (a) Original images, (b) noisy images and (c) Filtered images

### CONCLUSION

Two different techniques were proposed in the present work for mean filtering system design, to simplify hardware complexity, minimize processing time and improve overall system performance.

The reduction of hardware complexity can be achieved due to the efficient minimization in the required numbers of adders. The proposed improved method reduces the used number of adders from (n\*n-1) to 2\*(n-1) and the proposed systolic technique consumes as less as (n-1) adders only. If a storage element is used to keep the previous calculated sum in each step then further reduction can be gained to get (n) adders for the proposed improved method.

The division operations have been canceled in the proposed design method which results of more reduction in the used hardware as well as high improvements of processing time can be gained. This cancellation is done by storing the expected integer results of a division operation for different ranged sums.

The proposed methods and the implemented systems presented in this work were tested for different image sizes with varying filter length ranged from 3\*3 to 11\*11. The obtained results and plots ensure excellent improvements with respect to hardware design.

Execution time was effectively reduced with the proposed techniques especially for large images, that encourage to depend the proposed systems for many real time applications of image processing.

The filtered images, that differ clearly in their complicated characteristic details, show that the mean filter behaves better with images of complicated details.

Finally, the effect of Gaussian noise on gray images is greater than its effect on colored ones, but mean filter shows best performance with colored images than the enhancement achieved for gray images.

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