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## PC-Based Instrumentation System for the Study of Bean Cooking Kinetic

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**Abstract:** The aim of this study was to design and develop an electronic device, which allows the follow up of the complete cooking kinetic of bean and could be used to efficiently measure hard to cock (HTC). A prototype device composed of a modified Mattson bean cooker, interfaced with a computer via displacement sensors and a 4-channel, 8 bits, 1 Hz data acquisition module built around the MIC 640 was achieved. Cooking data obtained with this device shown that the beginning and the end of the cooking period separated a transition region portraying an exponential rise characterized by a time constant, which varies for freshly harvested beans (3 sec) as opposed to that of long stored beans (10 sec). This time constant, identified and measured for the first time, could be used to elaborate an index of the degree of hardening that has taken place in a given sample of beans.

Key words: PC data acquisition, cooking kinetic, cooking time constant, Mattson cooker, beans

#### INTRODUCTION

Cooking time as well as cooked texture, appearance and flavour are considered to be important cooking quality characteristics. Cooking time (Hsieh et al., 1993; Castellanos-Ramos and Guzman-Maldonado, 1995) and protein content (Guzman-Maldonado et al., 2000; De Almeida Costa et al., 2006) are important characteristics in bean breeding. The consumer desires grains with fast imbibition and short cooking times; that produces broth with good appearance, taste and flavour and with thin seed coat (Jacinto-Hernandez et al., 2002). Long cooking times are a major constraint to wider acceptance and consumption of grain legumes. Furthermore, overcooking of grain legumes has been reported to result in a reduction in the nutritive value of the protein. Several methods for measuring the cooking time of grain legumes have been reported. Some methods are subjective, in particular the sensory analysis and the tactile method; others are objective and use the Mattson cooker. The cooking time has been expressed in a number of different ways in the literature (Wang and Daun, 2005). Some researchers record the number of beans that has been penetrated by the plungers at the end of each minute. The cooking time for a sample is then taken as the time required for 50% of the beans to be penetrated. Others report cooking time as the time required for 100% of the beans to be penetrated or as the point where 92%

of the beans were penetrated. The objective methods are based on systems of detection of the cooking of grain legumes using a sensor, which gives information on cooked or uncooked state of the grain legumes, but does not provide any information on the intermediate state. These systems of detection of cooking do not therefore allow the study of the complete cooking kinetic of grain legumes. The aim of this work was to design and develop an electronic device that allows the follow up of the complete cooking kinetic of bean.

### MATERIALS AND METHODS

**Development of the PC-based instrument:** The prototype PC-based instrument developed is shows in Fig. 1. It is composed of a Mattson cooker, a Personal Computer (PC), sensors and a data acquisition module. The Mattson cooker (Fig. 2) consists of 19 plungers and a cooking rack with 19 reservoir-like perforated saddles, each of which holds a seed and a plunger calibrated to  $82 \times 10^{-3}$  kg. Each plunger terminates in a stainless steel probe (Fig. 2b, c)  $1.6 \times 10^{-3}$  mm in diameter. The seeds were positioned in the saddles of the rack so that the tip of each plunger rested on top of the seed. The rack was then placed in a  $2.15 \times 10^{-3}$  m³ metal beaker containing  $1.2 \times 10^{-3}$  m³ of boiling water. When a seed becomes sufficiently tender, the plunger penetrates the seed and drop through the hole in the saddle.

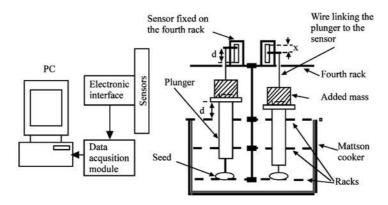


Fig. 1: Schematic block diagram of the PC-based bean cooker system

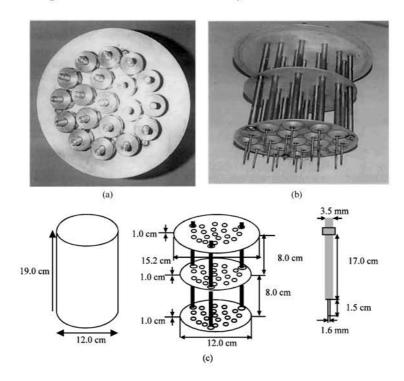


Fig. 2: The Mattson bean cooker (a) top view (b) the racks and the nineteen plungers (under view) and (c) the size

In this study, the Mattson cooker was modified as shown in Fig. 3. A fourth rack was added, onto which were fixed four displacement sensors. Such sensors with high accuracy (resolution:  $0.9 \times 10^{-3}$  m) and long range  $(15 \times 10^{-3}$  m) are reported in the literature (Kim *et al.*, 2006; Kuijpers *et al.*, 2006a, b). In order to achieve a simple and low cost device, the rectilinear displacement (x in Fig. 1) of the plunger penetrating the seed was converted into voltage using a potentiometer as shown in Fig. 4a, according to basic laws of electrical circuits:

$$V = +5\frac{\rho R}{\rho R + (1 - \rho)R} = +5\rho$$

### Where:

R = Value of the potentiometer

 $\rho$  = Which depends on the cursor position of the potentiometer, satisfy  $\rho \le 1$ 

This conversion is obviously linear (V = 0.122x + 0.116,  $r^2 = 0.987$ ) as shown experimental results plotted in Fig. 4b. The plunger weight was increased (Fig. 1) to  $231 \times 10^{-3}$  kg, in order to drop freely when a seed becomes sufficiently tender.

The analogical signal coming from each sensor was converted into numerical signal and transmitted to a PC through a 4-channel, 8 bits, 1 Hz data acquisition board.

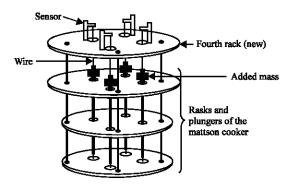
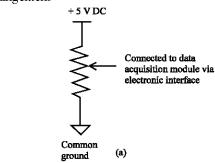


Fig. 3: The modified Mattson bean cooker: new rack arrangement



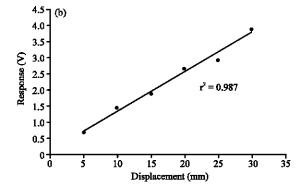


Fig. 4: The sensor (a) electrical circuit and (b) characteristics

This data acquisition board is built around the MIC 640 (IC1 in Fig. 5), a programmable integrated circuit developed by MICTRONICS®. The MIC 640 has four analogical inputs and one standard asynchronous serial output compatible TTL and CMOS. It can function in automatic mode and give a numerical signal at the rate of one sample per second (1 Hz), or in controlled mode and give a numerical signal at a rate determined by an external logical signal. In Fig. 5, the asynchronous serial output (pin number 2) of the MIC640 is connected to receive data (RD) pin (pin number 2) of a serial port via the resistor R1. The output serial data, transmit data pin (pin number 3) of

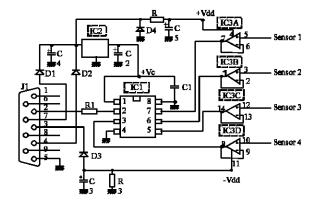


Fig. 5: Electrical circuit of the data acquisition board (Tavernier, 2005). Semiconductors: IC1 (MIC640); IC2 (LM 2936 Z5); IC3 (LF 444), D1, D2, D3, D4 (1N 4148). Resistors: R1 (1 k $\Omega$ ); R2 (47 k $\Omega$ ); R3, R4 (10 k $\Omega$ ). Capacitors: C1 (10 nF); C2 (10 µF); C3, C4 47 µF; C5 (0.22 µF). Connector: J1 (Female DB9)

Table 1: Physical characteristics of the beans used

| Length       | Width                                    | Thickness                                                             |                                                                                                                                                                                         |
|--------------|------------------------------------------|-----------------------------------------------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| (mm)         | (mm)                                     | (mm)                                                                  | m (g)                                                                                                                                                                                   |
| 11.5±1.9     | $7.1\pm0.7$                              | 5.1±0.1                                                               | 0.375±0.045                                                                                                                                                                             |
| $11.5\pm2.7$ | $7.4 \pm 2.2$                            | $5.5\pm0.1$                                                           | $0.404\pm0.087$                                                                                                                                                                         |
| $11.9\pm1.2$ | $7.6 \pm 1.1$                            | $5.8\pm0.1$                                                           | $0.428\pm0.045$                                                                                                                                                                         |
| 12.9±0.8     | 7.9±1.5                                  | 6.1±0.1                                                               | 0.465±0.021                                                                                                                                                                             |
|              | (mm)<br>11.5±1.9<br>11.5±2.7<br>11.9±1.2 | (mm) (mm)<br>11.5±1.9 7.1±0.7<br>11.5±2.7 7.4±2.2<br>11.9±1.2 7.6±1.1 | (mm)         (mm)         (mm)           11.5±1.9         7.1±0.7         5.1±0.1           11.5±2.7         7.4±2.2         5.5±0.1           11.9±1.2         7.6±1.1         5.8±0.1 |

the serial port, when connected to a PC, D3, R3 and C3 as shown in Fig. 5 produce the negative supply voltage-Vdd by half-wave rectification and filtering. The control lines DTR (Data Terminal Ready, pin number 4) and RTS (Request To Send, pin number 7) of the serial port provide the positive supply voltage + Vdd via D1, D2 and C4. This positive supply voltage is controlled with the 5 volts regulator LM2936Z5 (IC2) in order to supply the MIC 640 with a very stable voltage. A software program was developed in Turbo C++ version 3.0, which inputs serial data directly into the PC (DELL Pentium® II).

The bean sample: The beans (*Phaseolus vulgaris*) were obtained from farm samples in Ngaoundéré (Cameroon). Broken seeds, damaged seeds and foreign material were handpicked from the samples. Four samples of twenty freshly harvested seeds each one and four samples of twenty stored seeds (3 weeks at the room temperature) each one, having on average the same weight and the same morphological characteristics were considered (Table 1).

**Methods:** The stored beans and the freshly harvested beans were cooked separately, after being soaked in water during 12 h. Four seeds coming from each sample of stored beans were cooked together; in the same way four

seeds coming from each sample of freshly harvested beans were cooked together. During cooking, the positions of the four plungers of the modified Mattson cooker placed each one on a seed were recorded second by second and stored on a PC. The data stored were graphically plotted for each grain using SigmaPlot® 8.0 software.

### RESULTS AND DISCUSSION

The curves plotted in Fig. 6 and 7, respectively show the kinetic of cooking of four freshly harvested seeds and four stored seeds obtained with the PC-based instrumentation developed. Each curve is based on several thousands of positions of the plunger measured second by second. Each of these curves has three characteristic regions: two steady regions separated by a transition region. The first region corresponds to the beginning of the cooking period; the plunger does not move yet because the seed is not yet tender. The duration of this region is not the same for the four seeds. The second region corresponds to the end of cooking; the plunger has fully dropped. This region appears more or less quickly, according to whether the seed cooks more or less quickly. The time when the second region begins is the cooking time of the seed. The transition region corresponds to the penetration of the plunger in the seed. This penetration is done in a sudden way, like an avalanche, until complete cooking. The bean cooking data recorded in this region follow an exponential law for all the seeds (Fig. 8,  $r^2 > 0.97$ ),  $x(t) = x_0 + a * \tau * (exp(t/\tau) - 1)$ , where x(t) is the position of the plunger at any time,  $x_0$  the position of the plunger at the beginning of the cooking period and  $\tau$  a time constant. The system was calibrated so that  $x_0$  gives the thickness T (Table 1) of the seed.

The cooking times of the seeds measured are displayed in Table 2. For freshly harvested beans, they vary between 40 and 46 min. Hentges et al. measured for the same variety of beans (Phaseolus vulgaris), freshly harvested and soaked during 16 h in deionised distilled water prior to cooking, cooking times varying between 30 and 43 min. Wang and Daun (2005) measured cooking times of less than 25 min after a steeping of 24 h in water. Since the steeping and soaking of bean reduces the cooking time (Hincks et al., 1987), low values of cooking time measured by Wang and Daun (2005) and Hentges et al. (1990) are justified. The cooking times measured for stored beans in our experiments (Table 2) vary between 121 and 133 min. Hentges et al. (1990) measured cooking times varying from 78.5 min to more than 458 min for the same variety of bean (Phaseolus vulgaris) months at 29°C. three We observe compared to freshly harvested beans. the

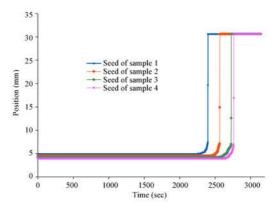


Fig. 6: Bean cooking kinetic of four freshly harvested seeds

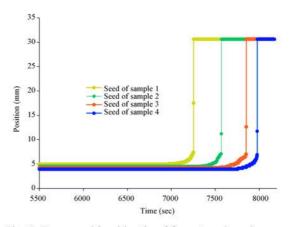


Fig. 7: Bean cooking kinetic of four stored seeds

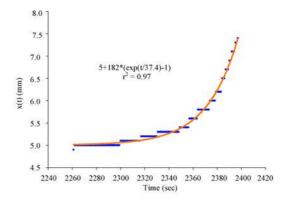


Fig. 8: Bean cooking kinetic in the transition region (freshly harvested seed of sample 1)

cooking time of stored beans is longer and depend on the shelf life. This observation is in agreement with earlier reports and is justified by the hard to cook phenomenon, which results from the physicochemical transformations, that undergoes the beans during the storage (Kyriakidis *et al.*, 1997; Berrios *et al.*, 1999).

Table 2: Cooking time of freshly harvested beans and three weeks stored beans at the room temperature

|                    | Seed of  | Seed of  | Seed of  | Seed of  |
|--------------------|----------|----------|----------|----------|
| Cooking time (sec) | sample 1 | sample 1 | sample 3 | sample 4 |
| Freshly harvested  | 2564     | 2397     | 2726     | 2761     |
| Three weeks stored | 7254     | 7569     | 7852     | 7977     |

Table 3: Cooking time constant  $\tau$  of freshly harvested beans and three weeks stored beans at the room temperature

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|------------------------------------------------|------------------|------------------|------------------|---------------------|--|--|
| Cooking time<br>constant (sec)                 | Seed of sample 1 | Seed of sample 2 | Seed of sample 3 | Seed of<br>sample 4 |  |  |
| Freshly harvested                              | 37.1             | 30.6             | 34.60            | 36.3                |  |  |
| Three weeks stored                             | 104.0            | 106.0            | 108.00           | 110.0               |  |  |
| $x_0 \text{ (mm)}$                             | 5.2              | 5.4              | 5.75             | 6.0                 |  |  |

Table 3 shows the time constant  $\tau$  measured in the transition region. It varies for the freshly harvested beans (35 sec) as opposed to that of stored beans (107 sec). This time constant, identified and measured for the first time, has been called the cooking time constant. Since it varies according to the shelf life of beans and consequently the physicochemical transformations that they undergo during the conservation, it could be used to elaborate an index of the degree of hardening that has taken place in a given sample of beans.

#### CONCLUSION

A PC-based system has been developed which allows the follow up of the bean cooking kinetic. It is a real-time system built around a PC and a 4-channel, 8 bits, 1 Hz data acquisition board. The kinetic plot obtained using this system has revealed a transition region between two steady regions corresponding to the beginning and the end of the cooking period. This transition region portrays an exponential rise characterized by a time constant which varies according to the shelf life of beans. This result suggests that this cooking time constant, identified and measured for the first time, could be used to elaborate an index of the degree of hardening that has taken place in a given sample of beans.

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